

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : YEH
Application No. : 10/601,701
Filed : June 24, 2003
Title : METHOD FOR FABRICATION OF
POLYCRYSTALLINE SILICON THIN
FILM TRANSISTORS
Group Art Unit : 2812
Examiner : S. Isaac
Docket No. : 3230-56

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**PETITION FOR EXTENSION OF
TIME UNDER 37 CFR § 1.136 (a)**

Sir:

Applicant hereby petitions pursuant to 37 CFR § 1.136 for a **TWO-MONTH** extension of time for responding to the outstanding Office Action of May 25, 2005. The period for response is accordingly hereby extended to expire on **October 25, 2005**, by the filing herewith of a Petition for a two-month extension of time and payment of the required fee, which is still within the six-month statutory period for response.

The required fee as established by 37 CFR §1.17(a), (b), (c) or (d) pursuant to 35 U.S.C. §41(a)(8) is: **\$225.00**. Authorization is hereby given to charge **Deposit Account No. 501874** in the amount of \$225 to cover this fee. ***A duplicate copy of this Petition is enclosed herewith.***

The Commissioner is also hereby authorized to charge any additional fees which may be required in connection with this petition to **Deposit Account No. 501874**.

10/25/2005 SZEWDIE1 00000070 501874 10601701

01 FC:2252 225.00 DA

Date: October 24, 2005

Respectfully submitted,

By:


Bruce H. Troxell
Reg. No. 26,592

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